

SWARNANDHRA COLLEGE OF ENGINEERING & TECHNOLOGY

ACCREDITED BY NATIONAL BOARD OF ACCREDITATION, AICTE, NEW DELHI

ACCREDITED BY NAAC WITH "A" GRADE – 3.32/4 CGPA.

Recognized under 2(f) &12(B) of UGC Act 1956, Approved by AICTE, Permanently Affiliated to JNTUK, Kakinada

SEETHARAMPURAM,NARSAPUR-534280.W.G.DT.,

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

BOARD OF STUDIES P.G.

S.No	Name	Designation	Contact Details	Remarks
1	Mr.N.Srikanth	Associate Professor	Head of the Department, ECE, SCET, Narsapur	Chairman
2	Dr.R.S.Dubey	Associate Professor	Dept. of ECE, SCET, Narsapur	Internal Member
3	Mr. J.E.N.Abhilash	Associate Professor	Dept. of ECE, SCET, Narsapur	Internal Member
4	Mr. S.Srilali	Assistant Professor	Dept. of ECE, SCET, Narsapur	Internal Member
5	Ms.K.K.V.Girija	Assistant Professor	Dept. of ECE, SCET, Narsapur	Internal Member
6	Dr. M. Kama Raju	Professor	Dept. of EC,GEC, Gudlavalleru	Subject Expert Nominated by A.C
7	Dr. G. Sasi Bhushan Rao	Professor	Dept. of ECE, Andhra University, Visakhapatnam	Subject Expert Nominated by V.C
8	Dr. V.Rajesh	Professor & Principal	Usha Rama College Of Engineering & Technology,Vijayawada.	Subject Expert Nominated by A.C
9	Mr. V. Govinda Rao	Managing Director	M/S Microlink Peripheral Control Pvt. Ltd., Vijayawada	Industrial Expert
10	M. Satyanarayana	Physical Design Engineer	Socpronics Technologies Pvt. Ltd., Hyderabad	Alumni Member


HEAD OF THE DEPARTMENT


PRINCIPAL

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

MINUTES OF MEETING BOARD OF STUDIES

Minutes of third Board Of Studies meeting of Department of Electronics & Communication Engineering, Swarnandhra College of Engineering & Technology(Autonomous) on 19th June, 2016 at 10 AM.

UG Programme

BOS members advised to

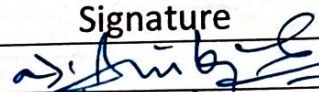
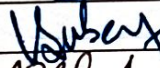

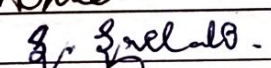
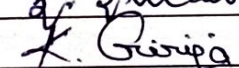
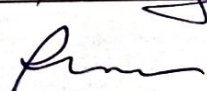
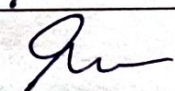
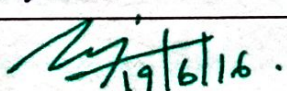
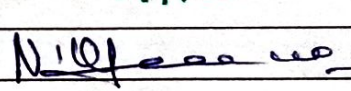
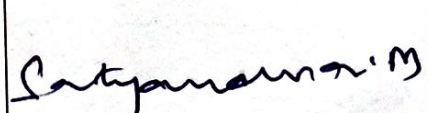
1. Change title of *Electrical Principles* subject
2. Change title of the *Digital Logic Design* subject (suggested 'Digital Electronics')
3. Schedule lab in the next semester after the completion of related subject
4. Incorporate *Virtual labs* (software versions) in the laboratories, wherever possible
5. Mandate the students to do *macro project* at the end of the lab, wherever possible
6. Change the title of subject '*Essentials of Analog Communications*' to '*Principles of Analog Communications*'
7. Replace '*OOPS Through C++*' with *Electronic Devices and Circuits* in the second semester
8. Check with government gazette to give some *credits to Mandatory courses* like Professional Ethics
9. Place Nano Technology , MATLAB and Robotics lab etc., in the Skill based laboratory elective

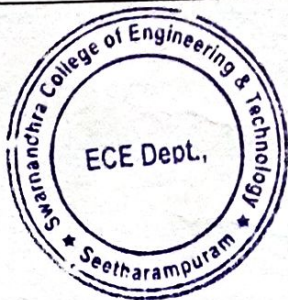
Minutes of third Board Of Studies meeting of Department of Electronics & Communication Engineering, Swarnandhra College of Engineering & Technology(Autonomous) on 19th June, 2016 at 1 PM.


PG Programme

BOS members advised to

1. Conduct International level conferences in the department by getting fund from government agencies
2. Improve industry – institution interaction for excellence by talking MOUs and implement given suggestions
3. Encourage the students to publish the papers in the reputed journals

s.no	Name of the BOS member	Designation in BOS	Signature
1	Mr. N Srikanth	Chairperson	
2	Dr. R S Dubey	Internal member	
3	Mr. J E N Abhilash	Internal member	
4	Mr. S Srilali	Internal member	
5	Ms. K K V Girija	Internal member	
6	Dr. M Kama Raju	Subject expert nominated by AC	
7	Dr. G Sasi Bhushan Rao	Subject expert nominated by VC	
8	Dr. V Rajesh	Subject expert nominated by VC	
9	Mr Govinda Rao	Industrial expert	
10	Mr. M Satyanarayana	Alumni member & Physical Design Engineer	




CHAIRMAN
BOARD OF STUDIES
Department of ECE
Swarnandhra College of Engineering & Technology